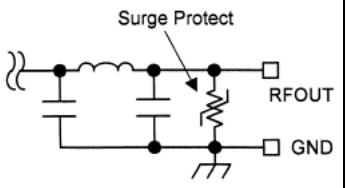
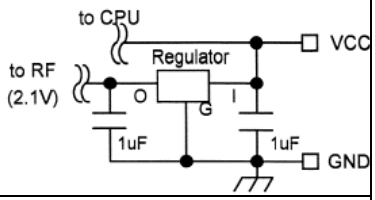
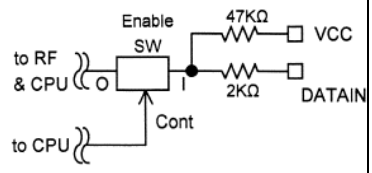
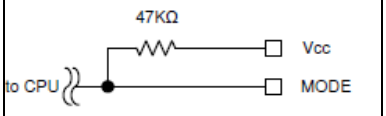
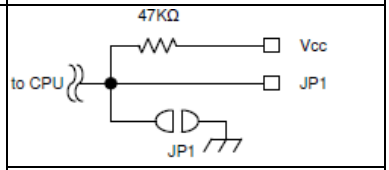
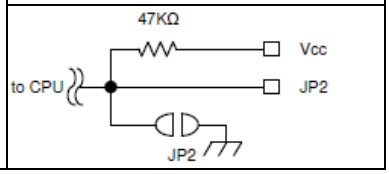
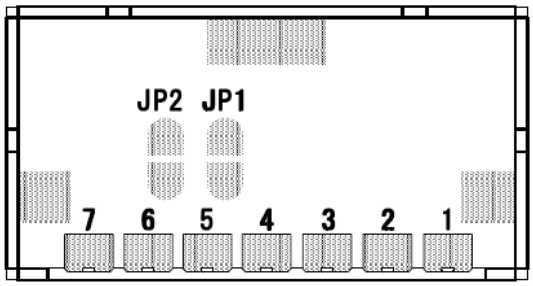
SUPPLEMENTAL INFORMATION to CDP-TX-05MP-R

This document is supplemental to the CDP-TX-05M-R and CDP-RX-05M-R operation guide and prepared for users of the CDP-TX-05MP-R.

PIN DESCRIPTION

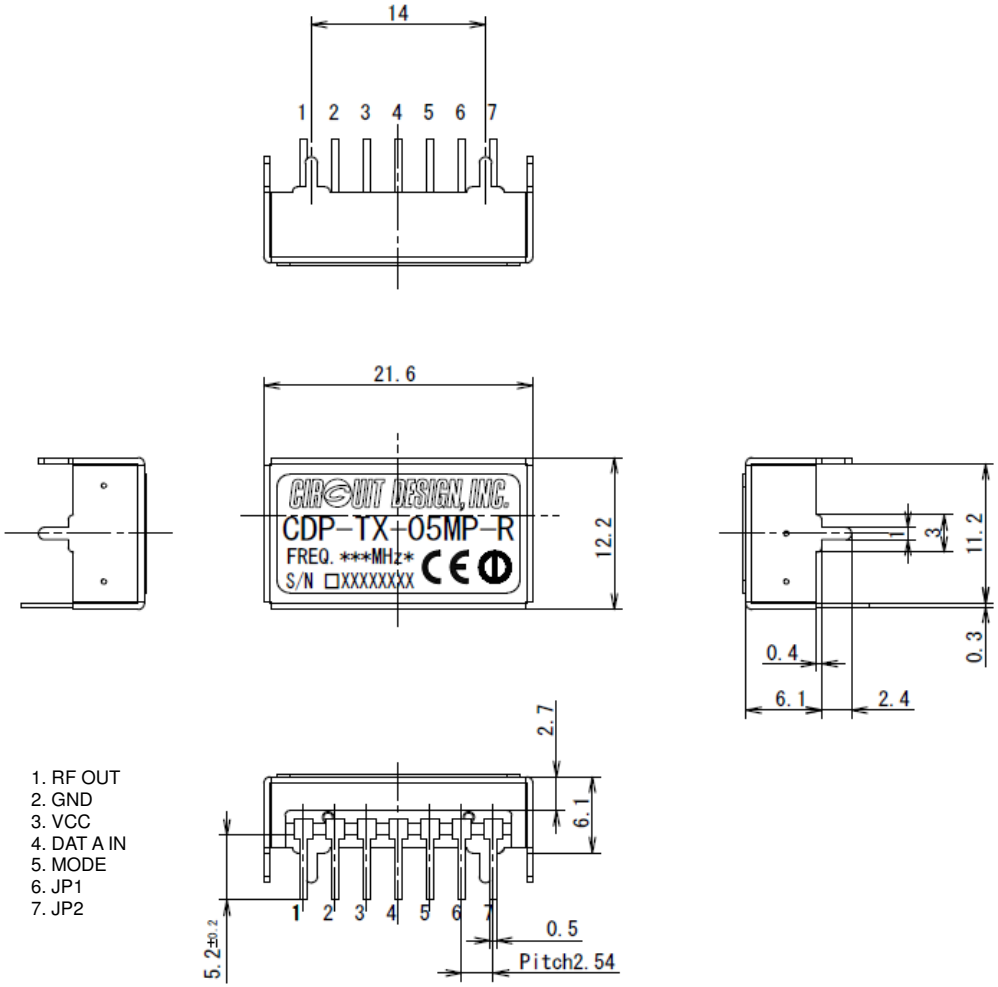
CDP-TX-05MP-R

Pin-No.	Pin-Name	I/O	Description	Equivalent internal circuit
1	RFOUT	O	Z=50 ohm The RF output power is 10 mW for 434 MHz and 5 mW for 869 MHz. 1/4 lambda whip antenna is recommended. The antenna length is 17.3 cm for 434 MHz and 8.6 cm for 869 MHz.	
2	GND	-	The ground Please connect to the widest GND on the PCB.	
3	VCC	-	The power supply terminal Operates on DC 2.2 V to 5.5 V. If the voltage becomes lower than 2.2 V, RF characteristics such as frequency stability will be affected.	
4	DATAIN	I	The data input terminal Digital input. Hi level = VCC Lo level = 0V Stable transmission will be obtained 20 ms (max.) after VCC is fed to the terminal. The maximum time for continuous High or Low signals must be within 20 ms. When this pin is open, the frequency has an offset drift. Once a standard code such as 511PNCODE has been input, the frequency will be within specifications.	
5	MODE	I	Leave this terminal open. L = Factory setting	
6	JP1	I	These terminals are pulled-up to the VCC. The frequency channel can be set as follows: Ch3 (JP1-Open, JP2-Open) / Ch2 (JP1-Short, JP2-Open) Ch1 (JP1-Open, JP2-Short) / Ch0 (JP1-Short, JP2-Short)	
7	JP2	I		



The solder jumpers JP1 and JP2 are internally connected to the pin # 6 and pin #7, respectively.

DIMENSIONS CDP-TX-05MP-R



1. RF OUT
2. GND
3. VCC
4. DAT A IN
5. MODE
6. JP1
7. JP2

Reference hole position for PCB mounting
(Top view)